TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

2SK2915

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• Low drain-source ON resistance : RDS (ON) = 0.31 Ω (typ.)

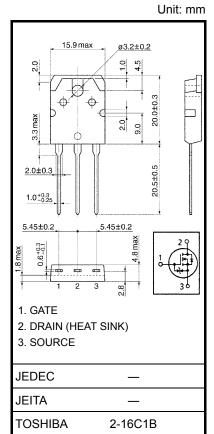
• High forward transfer admittance $: |Y_{fs}| = 15 \text{ S (typ.)}$

• Low leakage current $: I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 600 \text{ V)}$

• Enhancement mode : $V_{th} = 2.0 \sim 4.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	600	V	
Drain-gate voltage (R	_{GS} = 20 kΩ)	V_{DGR}	600	٧	
Gate-source voltage		V_{GSS}	±30	٧	
Drain current	DC (Note 1)	I _D	16	Α	
	Pulse (Note 1)	I _{DP}	64		
Drain power dissipatio	n (Tc = 25°C)	P _D	150	W	
Single pulse avalanche energy (Note 2)		E _{AS}	1026	mJ	
Avalanche current		I _{AR}	16	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	15	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.833	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 7.01 mH, R_G = 25 Ω , I_{AR} = 16 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



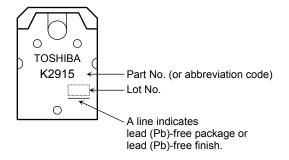
Electrical Characteristics (Ta = 25°C)

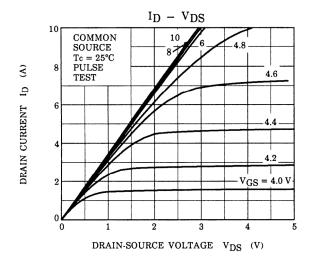
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source br	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	_	_	V
Gate threshold v	oltage/	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 8.0 A	_	0.31	0.4	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 8.0 A	8.0	15.0	_	S
Input capacitano	e	C _{iss}		_	3520	_	
Reverse transfe	Reverse transfer capacitance C_{rss} $V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		_	20	_	pF	
Output capacitance		Coss]		300		_
Switching time	Rise time	t _r	V_{GS} V_{OUT} V_{DD} V_{DD} V_{OUT} V_{DD}	_	50	_	- ns
	Turn-on time	t _{on}		_	100	_	
	Fall time	t _f		l	60		
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\mathbf{W}} = 10 \mu \text{s}$		325	_	
Total gate charge (gate-source plus gate-drain)		Qg			80		
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 16 \text{ A}$		48		nC
Gate-drain ("miller") Charge		Q _{gd}			32	_	

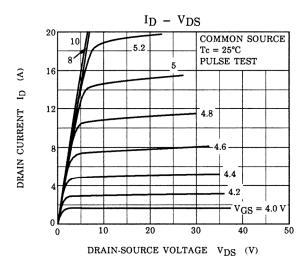
Source-Drain Ratings and Characteristics (Ta = 25°C)

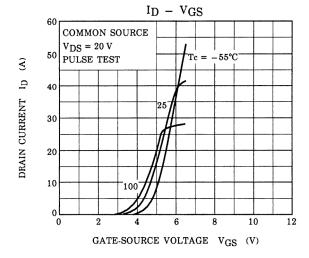
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	16	А
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	64	Α
Forward voltage (diode)	V_{DSF}	I _{DR} = 16 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 16 A, V _{GS} = 0 V		620	_	ns
Reverse recovery charge	Qrr	dI _{DR} / dt = 100 A / μs	1	7.5	_	μC

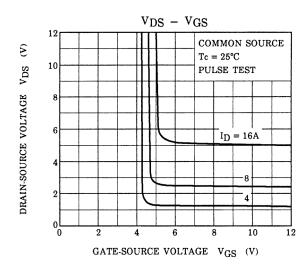
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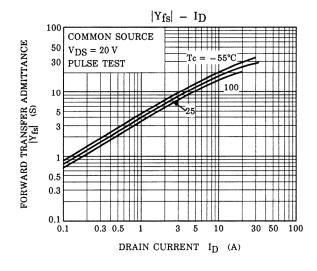


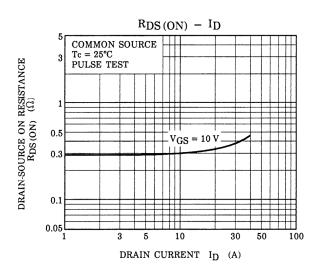


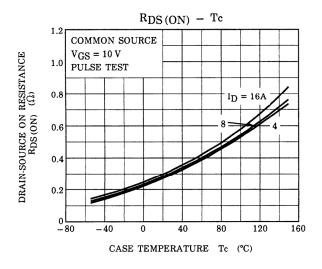


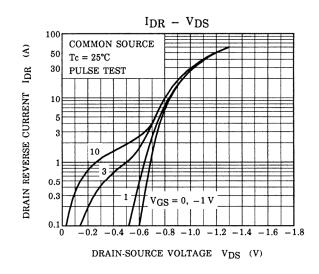


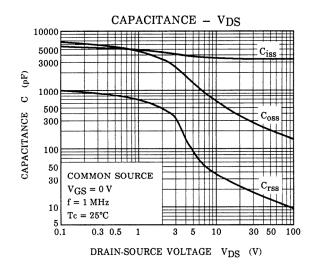


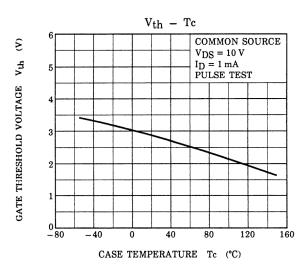


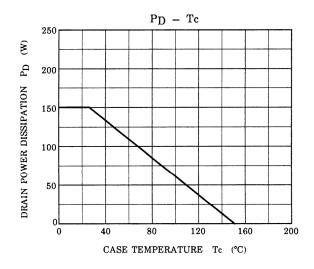


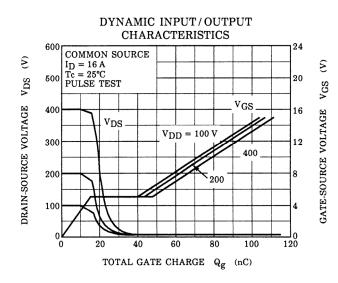


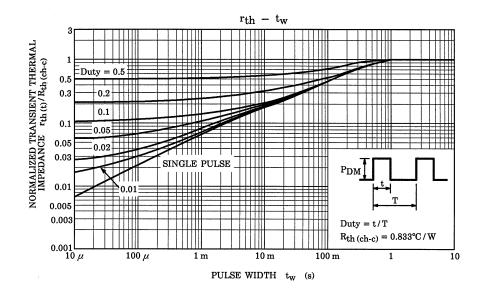


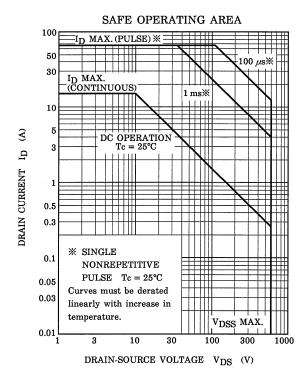


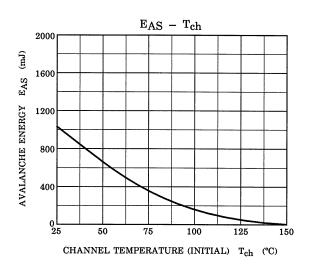


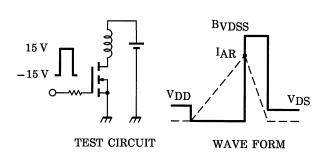












$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 90~V,~L = 7.01~mH \end{aligned} \qquad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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